

Leti Workshop on Innovative Memory Technologies to Include Presentations by STMicroelectronics, Numonyx, IBM and SAMSUNG

GRENOBLE, France – June 8, 2009 – Leti, a leading global research center committed to creating and commercializing innovation in micro- and nanotechnologies, is hosting a workshop on innovative memory technologies at MINATEC on Wednesday, June 24.

The workshop will cover recent advances in semiconductor memory technologies. Topics will range from short-term to long-term memory solutions, including:

- floating-gate and charge-trap non-volatile memories for embedded or stand-alone applications
- resistive memory technologies such as phase-change memories and oxide-based memories
- three-dimensional integration approaches to increase memory density
- innovative ideas covering thin-film memories, molecular memories and new architectures.

The workshop also will feature presentations by leaders in the global memory industry: STMicroelectronics, Numonyx, IBM and SAMSUNG.

The workshop is part of the 11th Leti Annual Review June 22-24. For more information about the annual review and the memory workshop, including registration options, visit leti.congres-scientifique.com/annualreview2009/.

About CEA/Leti

CEA-LETI, the Laboratory for Electronics & Information Technology is operated by Direction de la Recherche Technologique at CEA, the French Atomic Energy Commission. It mainly aims at helping companies to increase their competitiveness through technological innovation and transfer of its technical know-how to industry. Major player in the MINATEC Micro-Nano technologies innovation center, CEA-LETI benefits from 8000 m² state-of-the-art clean rooms, with equipment worth some 160 million euros. It is currently employing some 1600 people among whom 1100 CEA employees and co-workers of various status including 100 people from industrial partners, working in the CEA-LETI premises within the framework of bilateral collaborations. Overall, research contracts with industry are worth 75% of CEA-LETI annual income. It has a very important patents portfolio, and filed last year more than 200 patents and 700 publications. The laboratory is structured into six departments, with a specific department operating 24-7 the technological facilities of the silicon technology platform, and five program-oriented programs covering the field of microelectronics, Microsystems, optronics, system design and telco, and technologies for bio and health.

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Press Contacts:

CEA-Leti

Clément Moulet, Press Officer

Tel.: +33 4 38 78 03 26

E-mail: clement.moulet@cea.fr